

Serial No.:  
Inventor: R. Hoctor et al  
Docket No.: RD-27,855  
Attorney: J. Breedlove  
Atty's Tele. No.: 518.387.6276

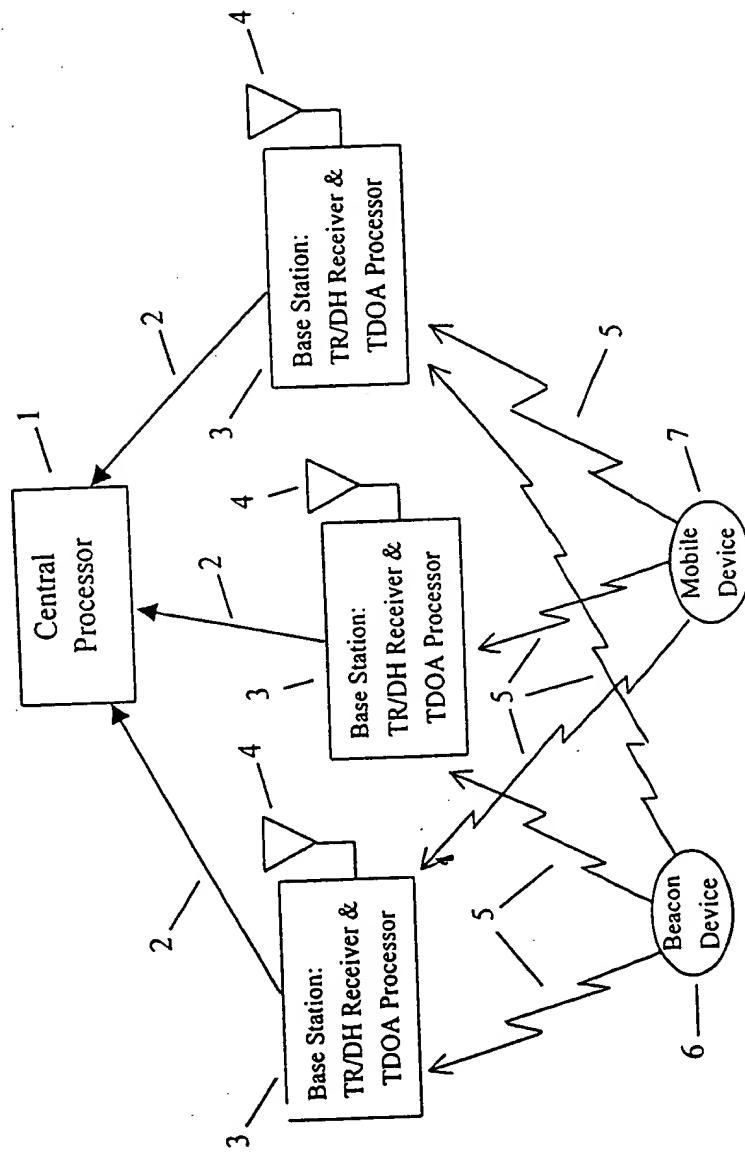


Fig. 1

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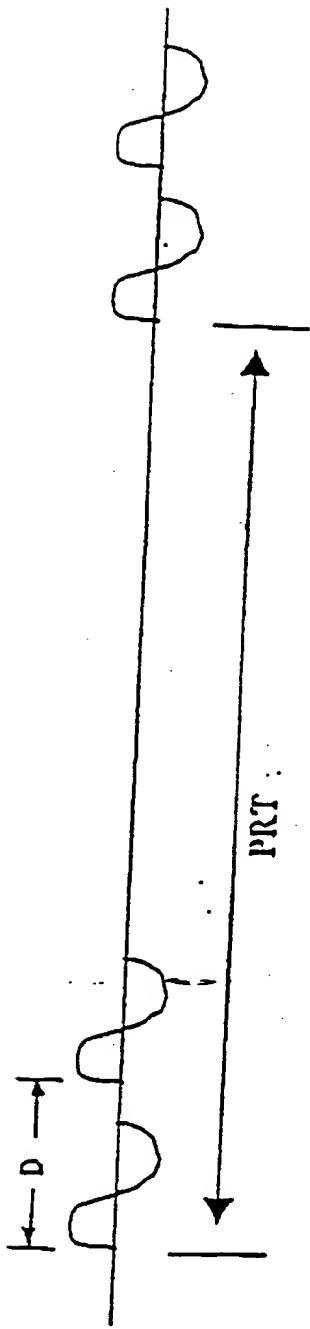


Fig. 2

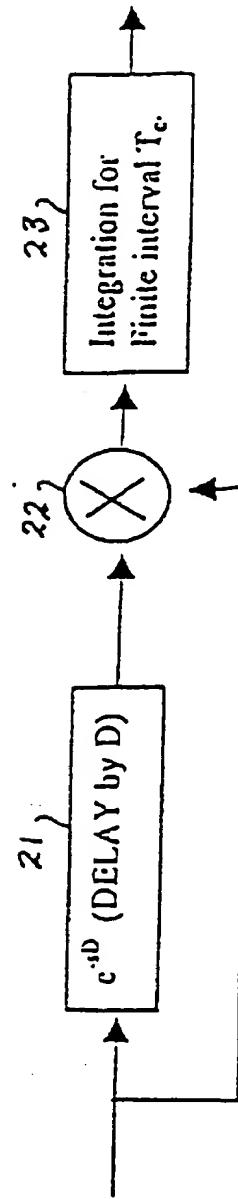


Fig. 3

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$N_p$ pulse pairs separated by time $D_1$ , with data bit $B_1$ .	$\dots$	$N_p$ pulse pairs separated by time $D_{(N_p-1)}$ , with data bit $B_{(N_p-1)}$ .	$N_p$ pulse pairs separated by time $D_{N_p}$ , with data bit $B_{N_p}$ .
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Fig. 4

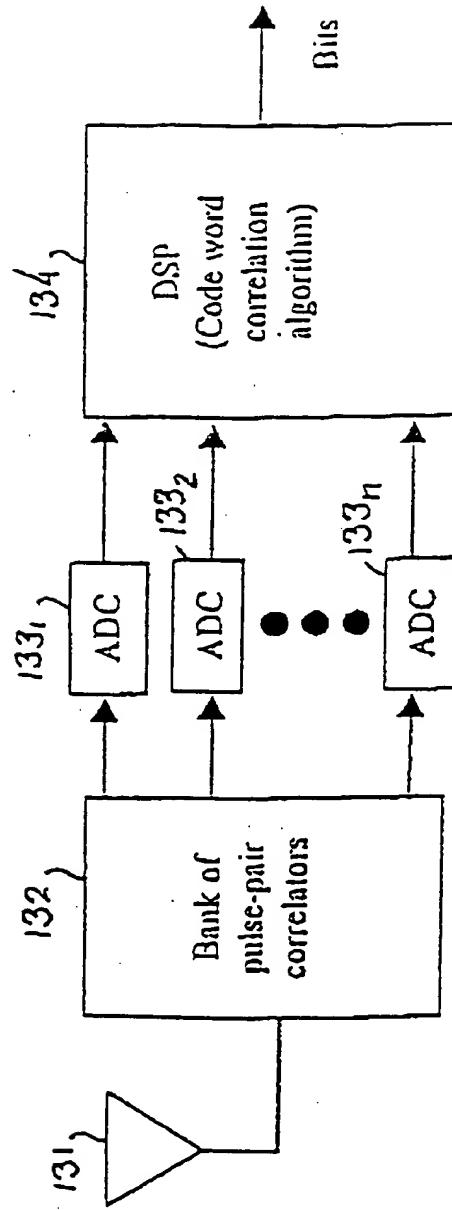


Fig. 5

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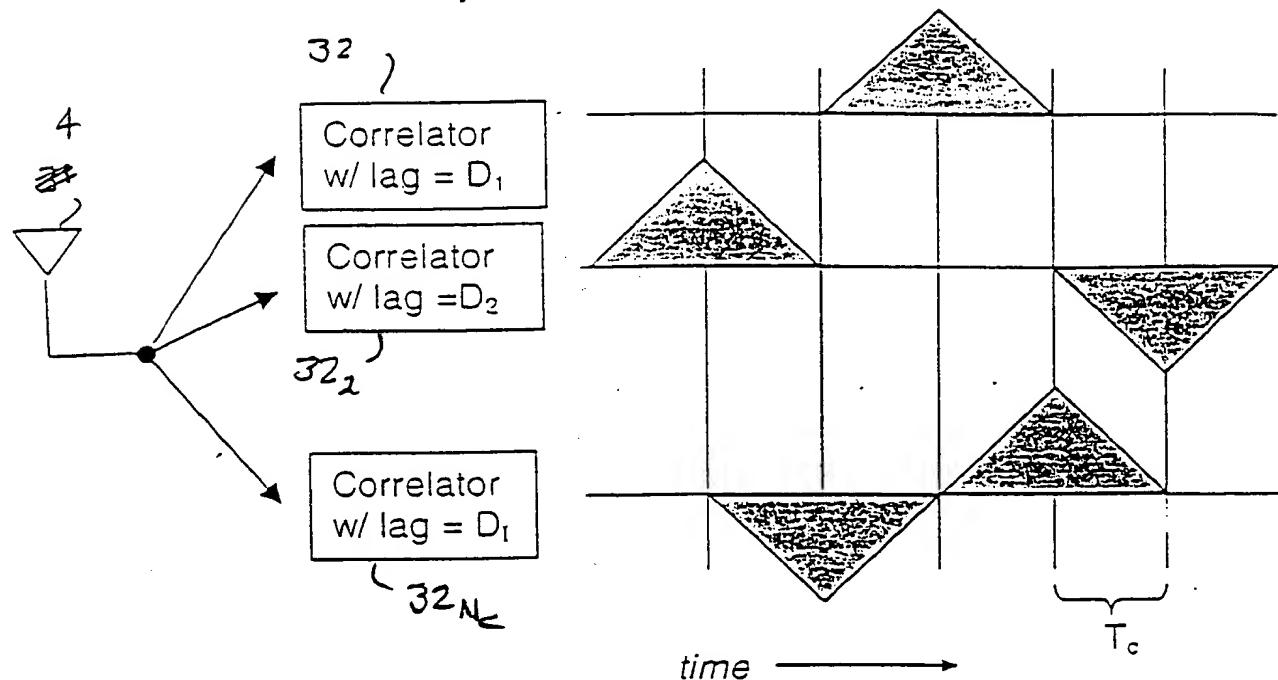


FIG. 6

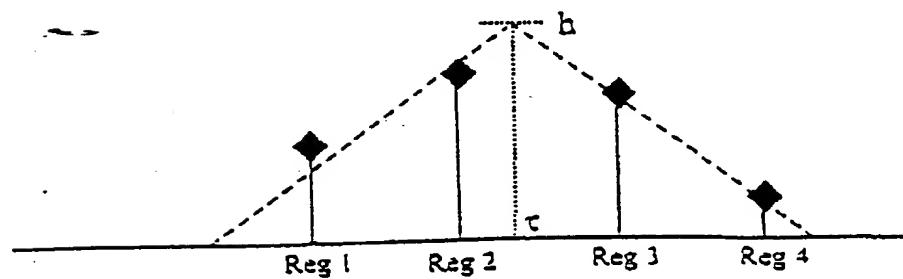


FIG. 7

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Outputs of pulse-pair  
correlators for a range of lags.

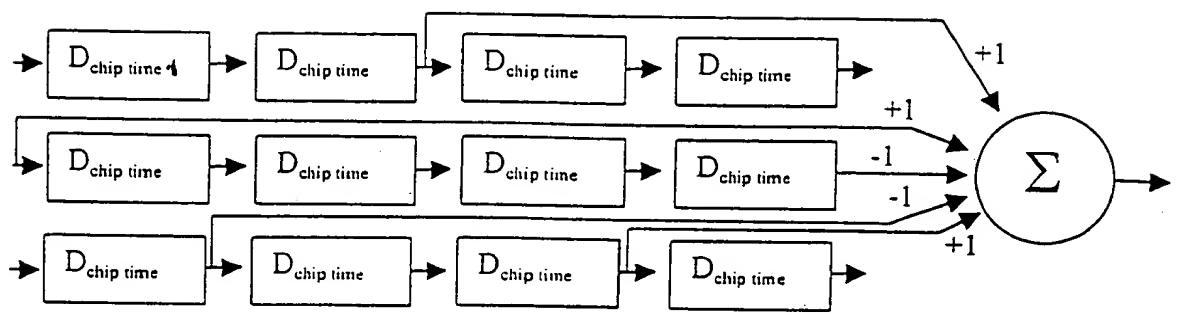


Fig. 8